

FIG. 1

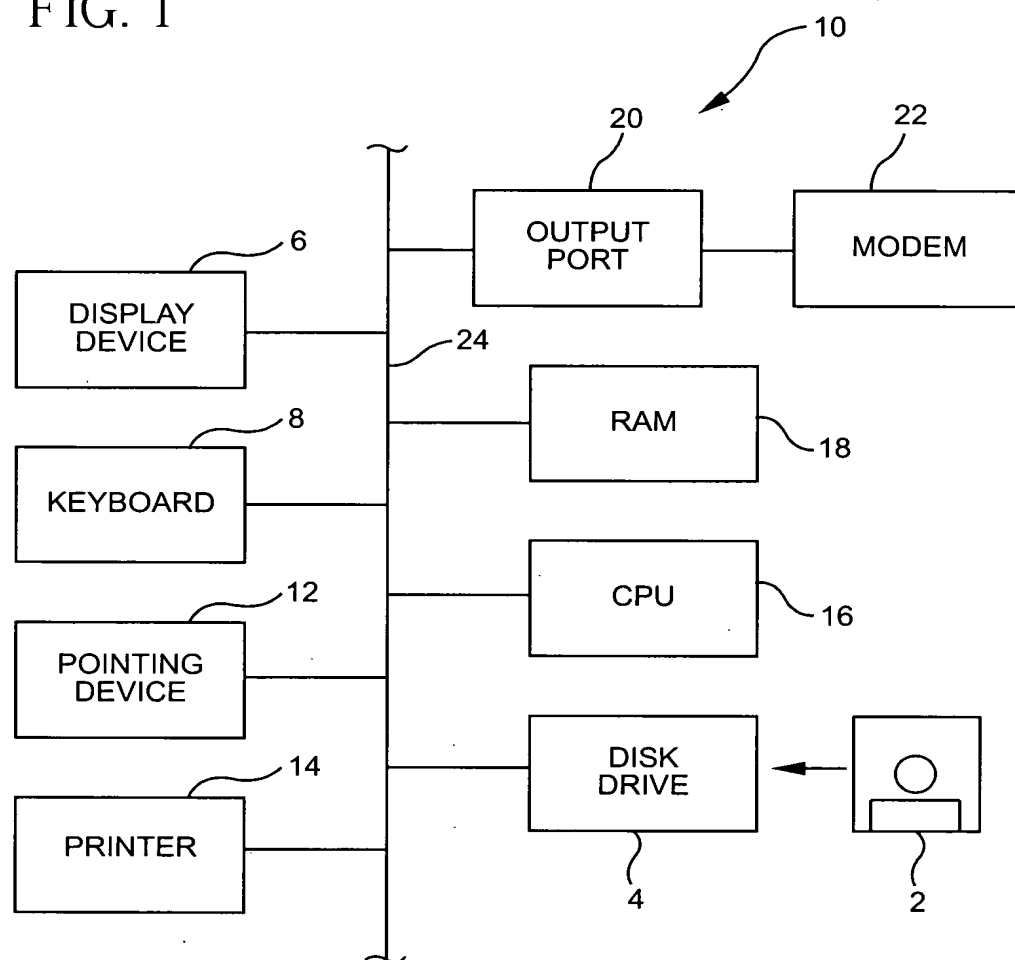


FIG. 2

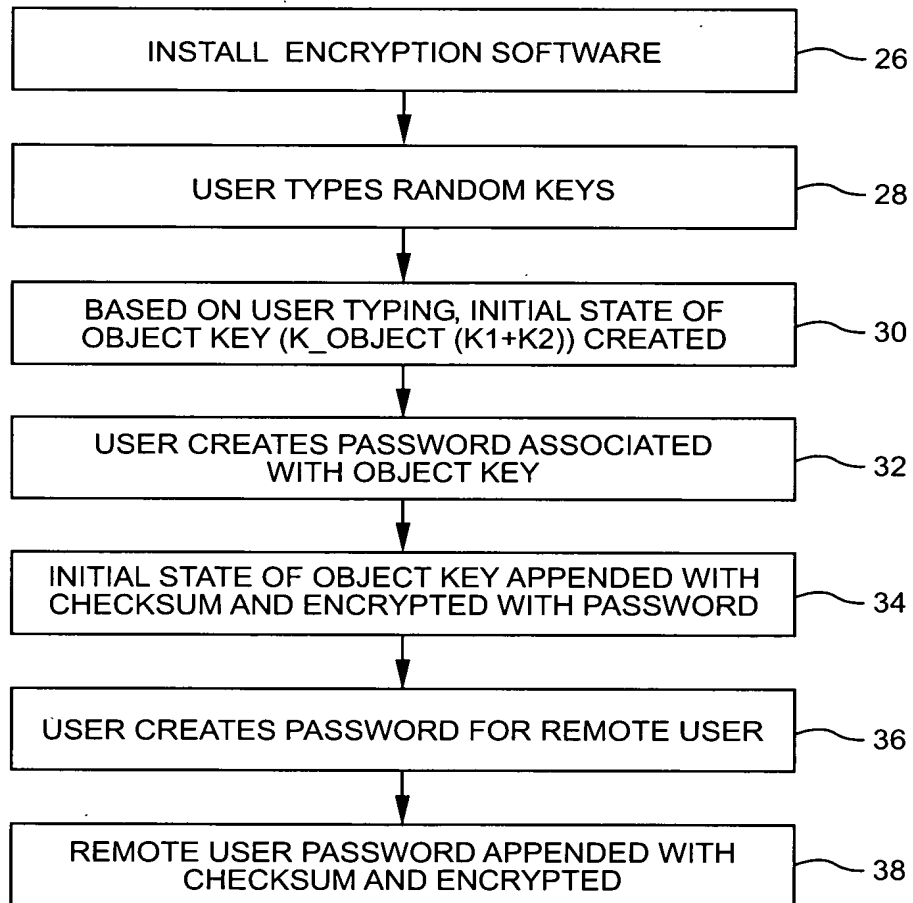


FIG. 3

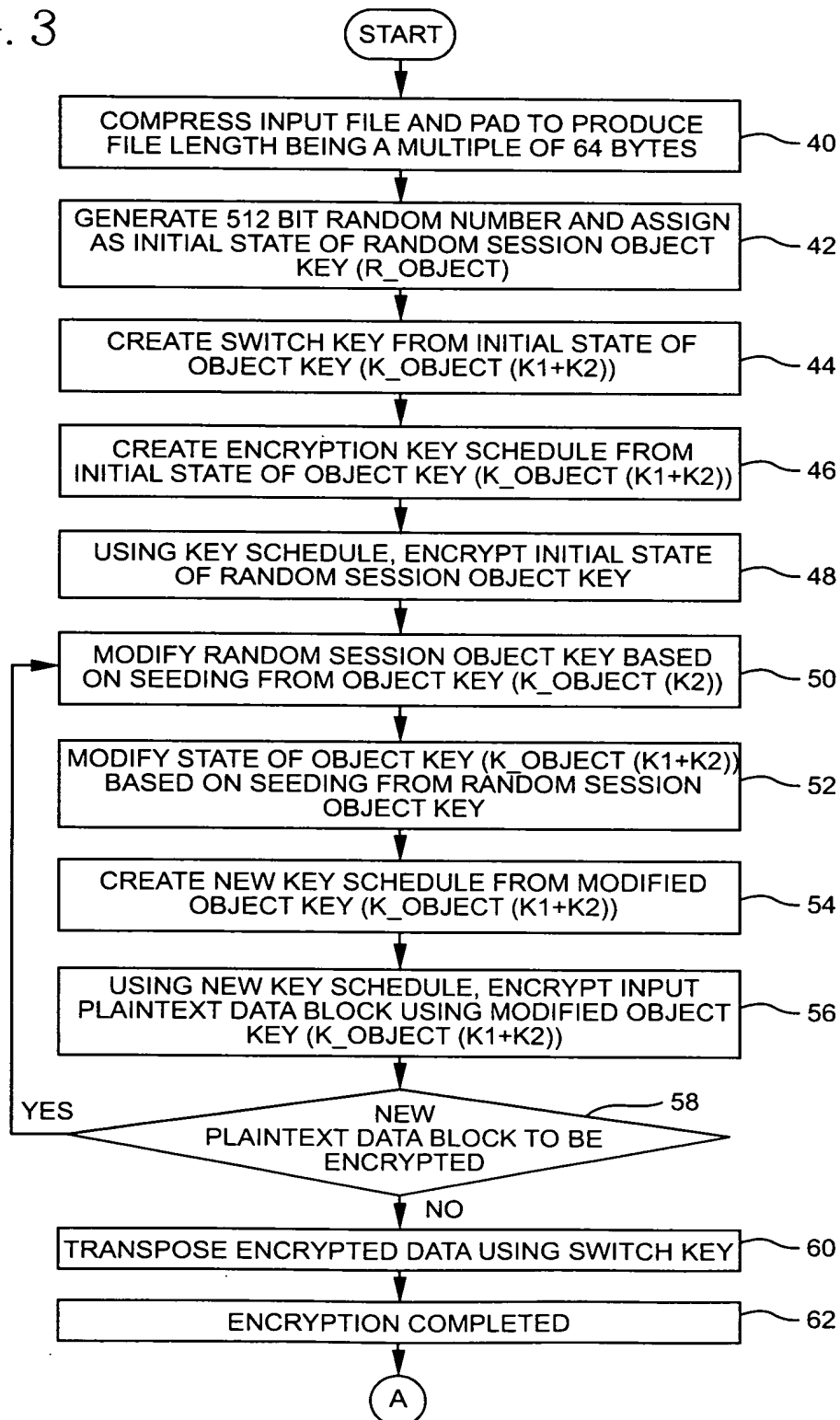


FIG. 4

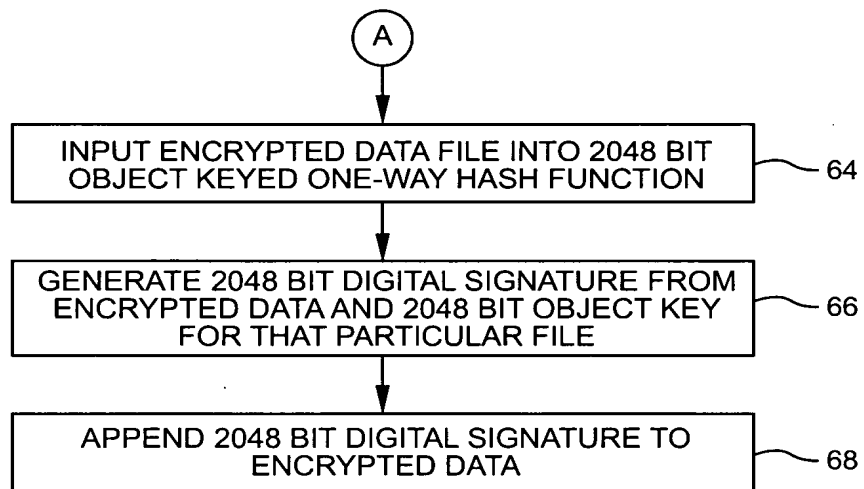


FIG. 5A

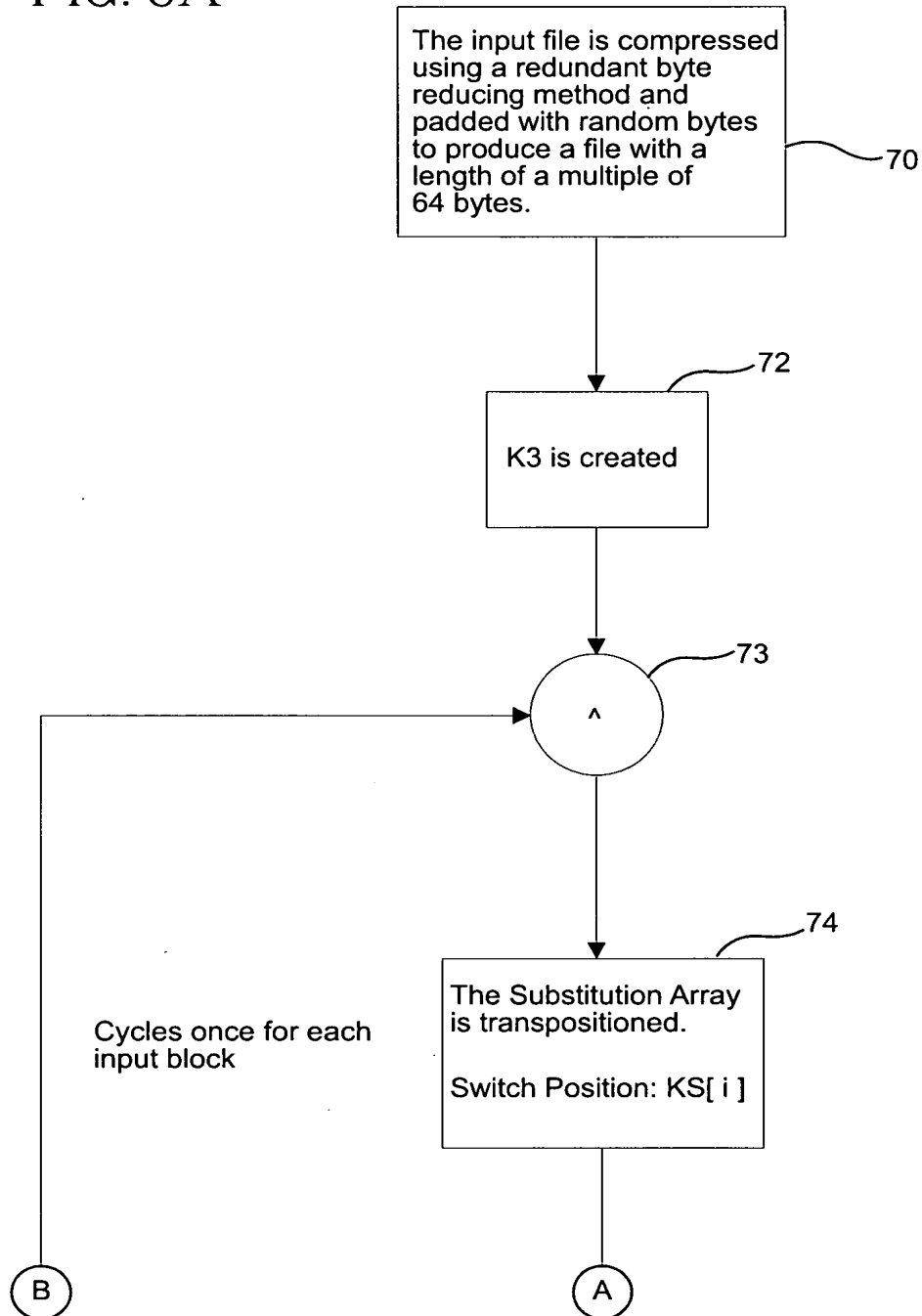


FIG. 5B

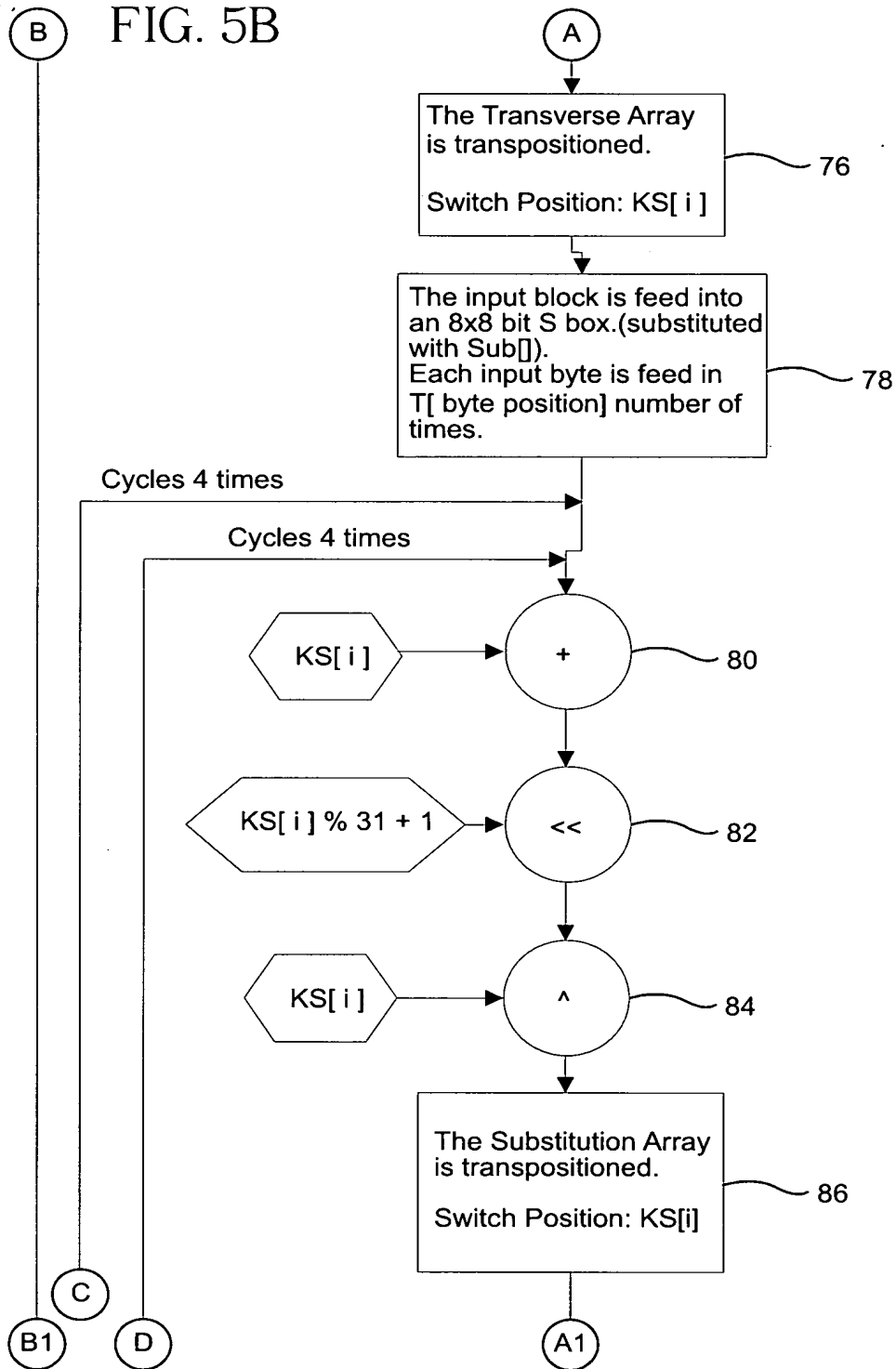


FIG. 5C

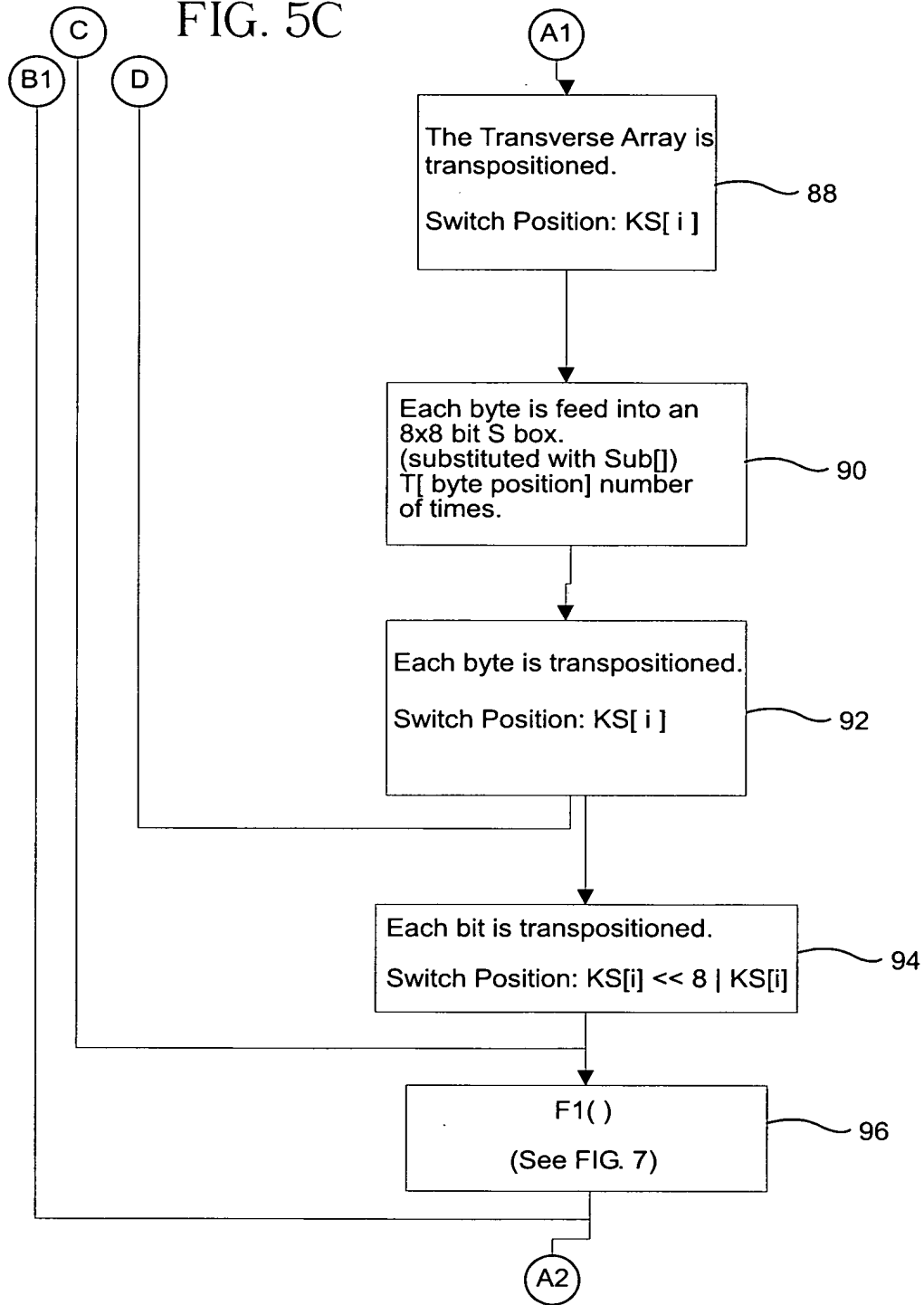


FIG. 5D

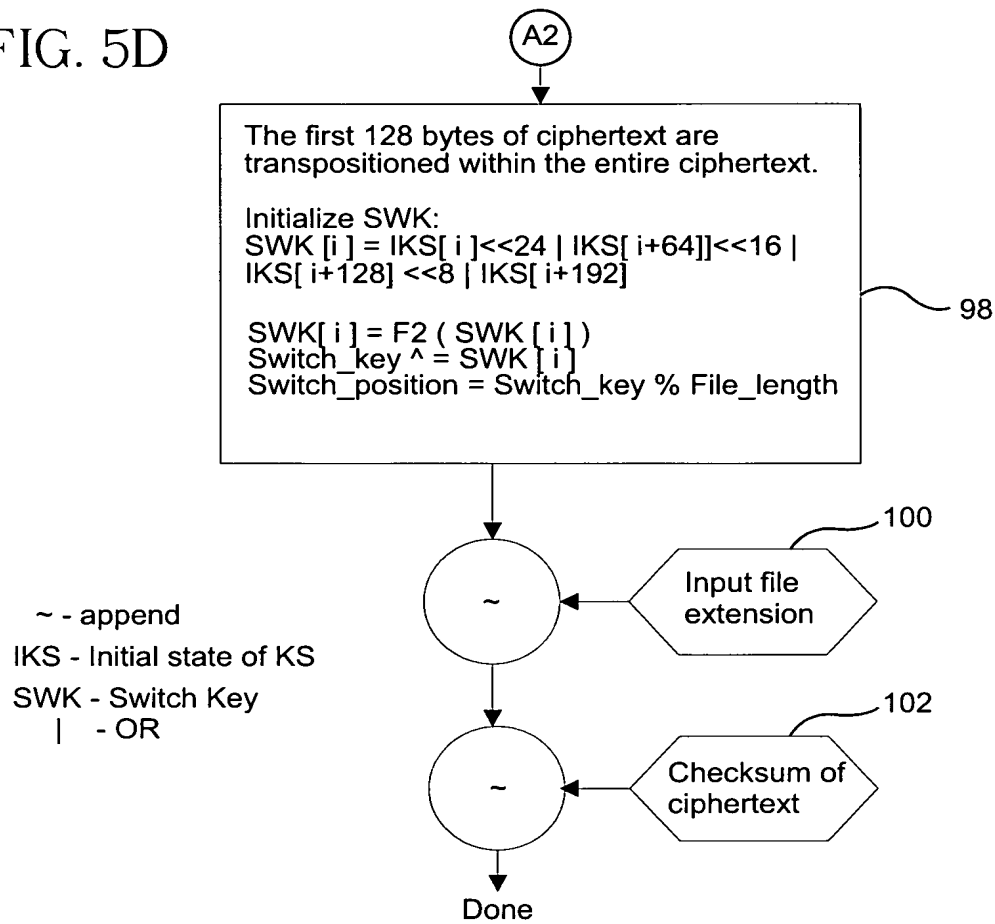


FIG. 6

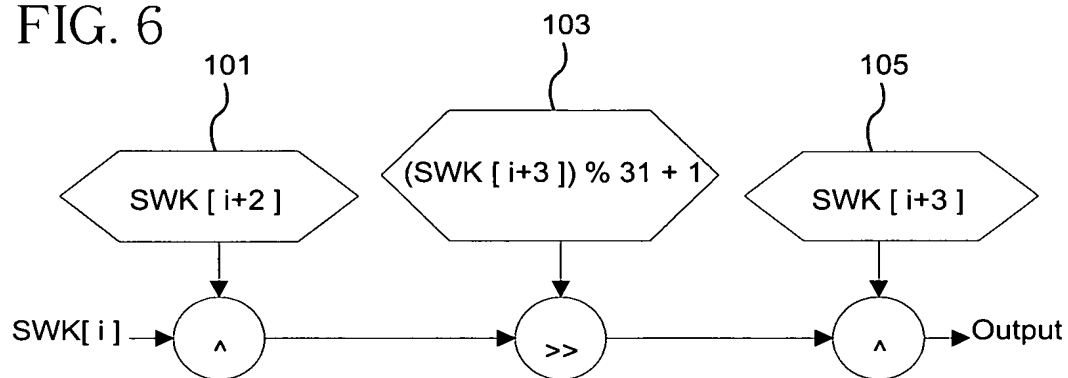




FIG. 7A

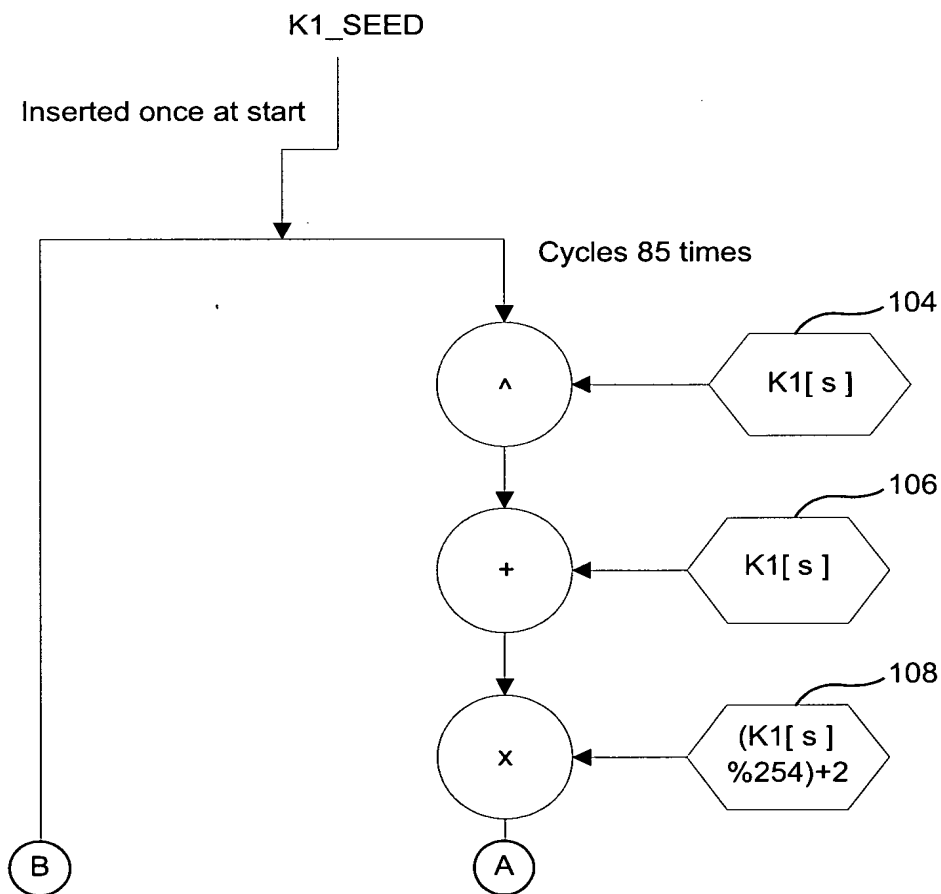
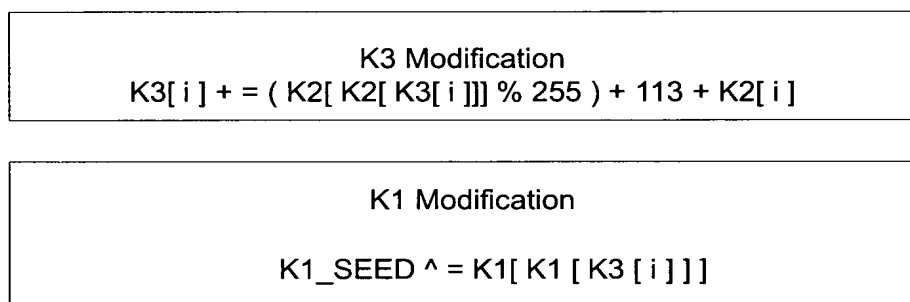


FIG. 7B

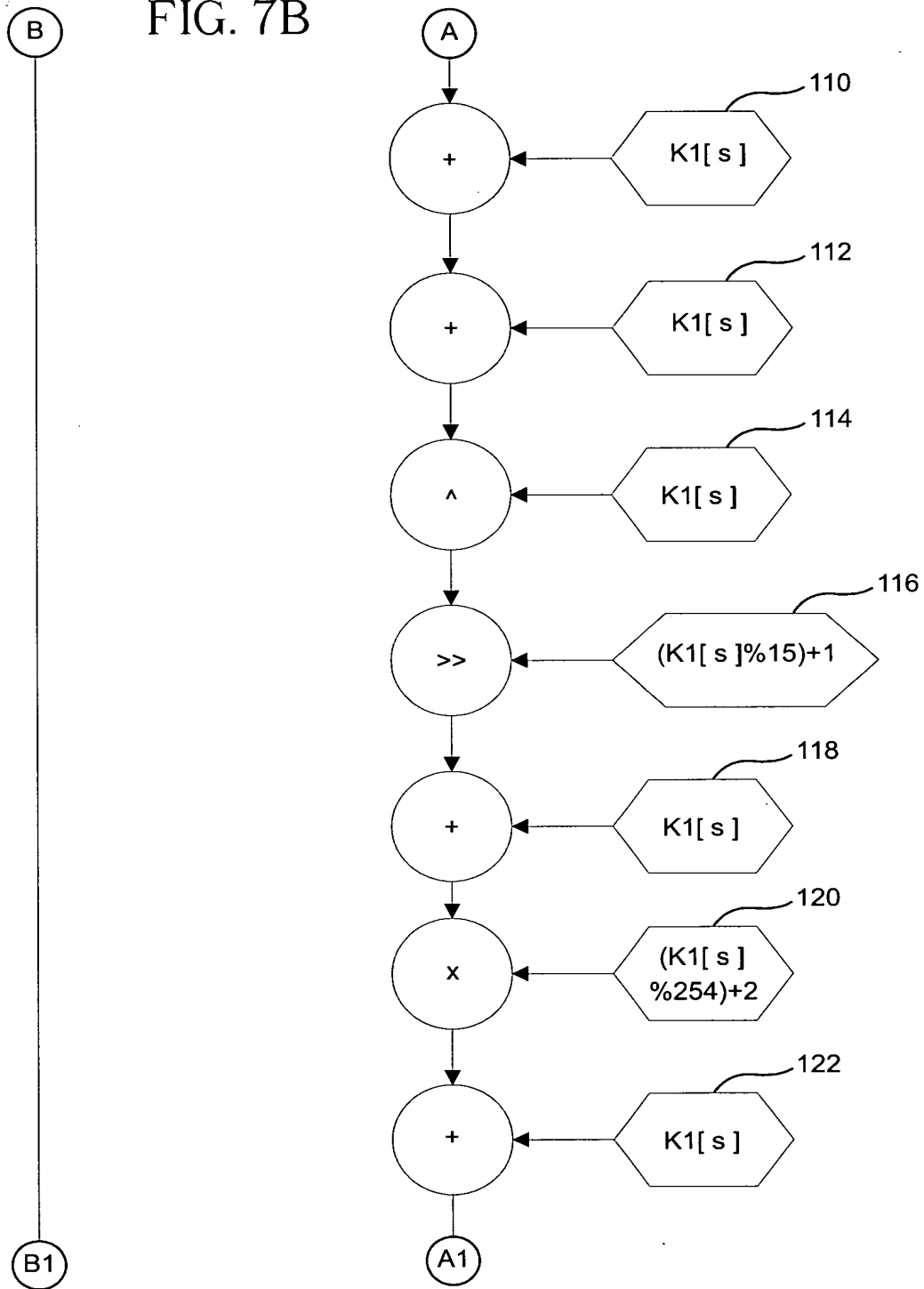


FIG. 7C

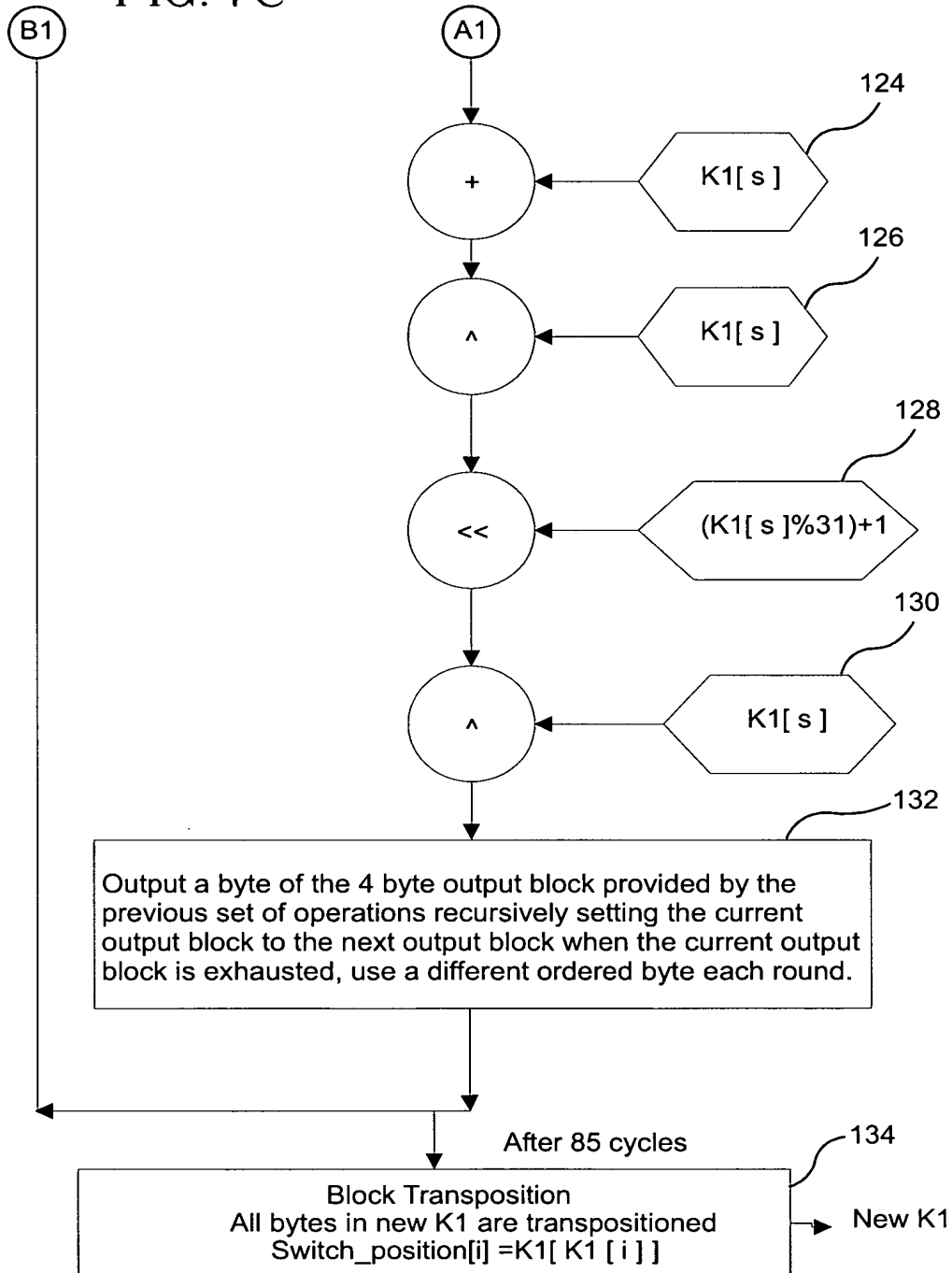


FIG. 8A

K2 Modification  
$$K2\_SEED += (K3[K3[\#] \% 64] \% 253) + 3$$
$$K2\_SEED \wedge = K2[K2[K3[K2[K3[s \% 64] + K2[\#] \% 192] \% 64]]]$$

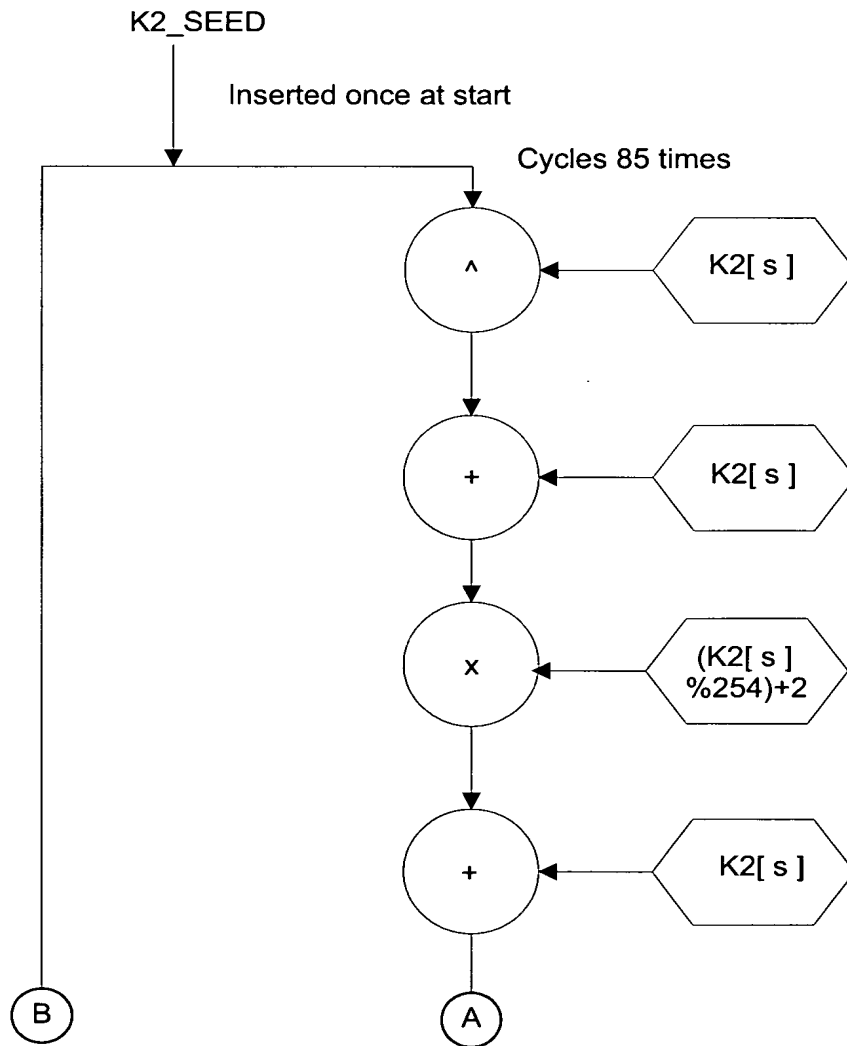


FIG. 8B

B

B1

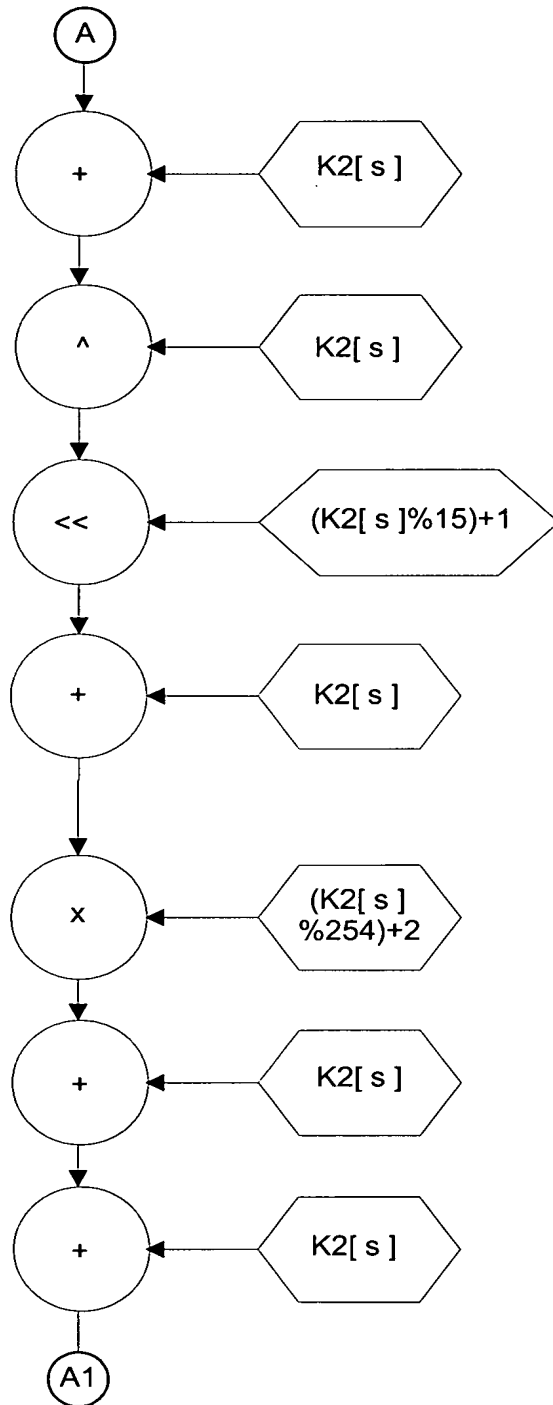


FIG. 8C

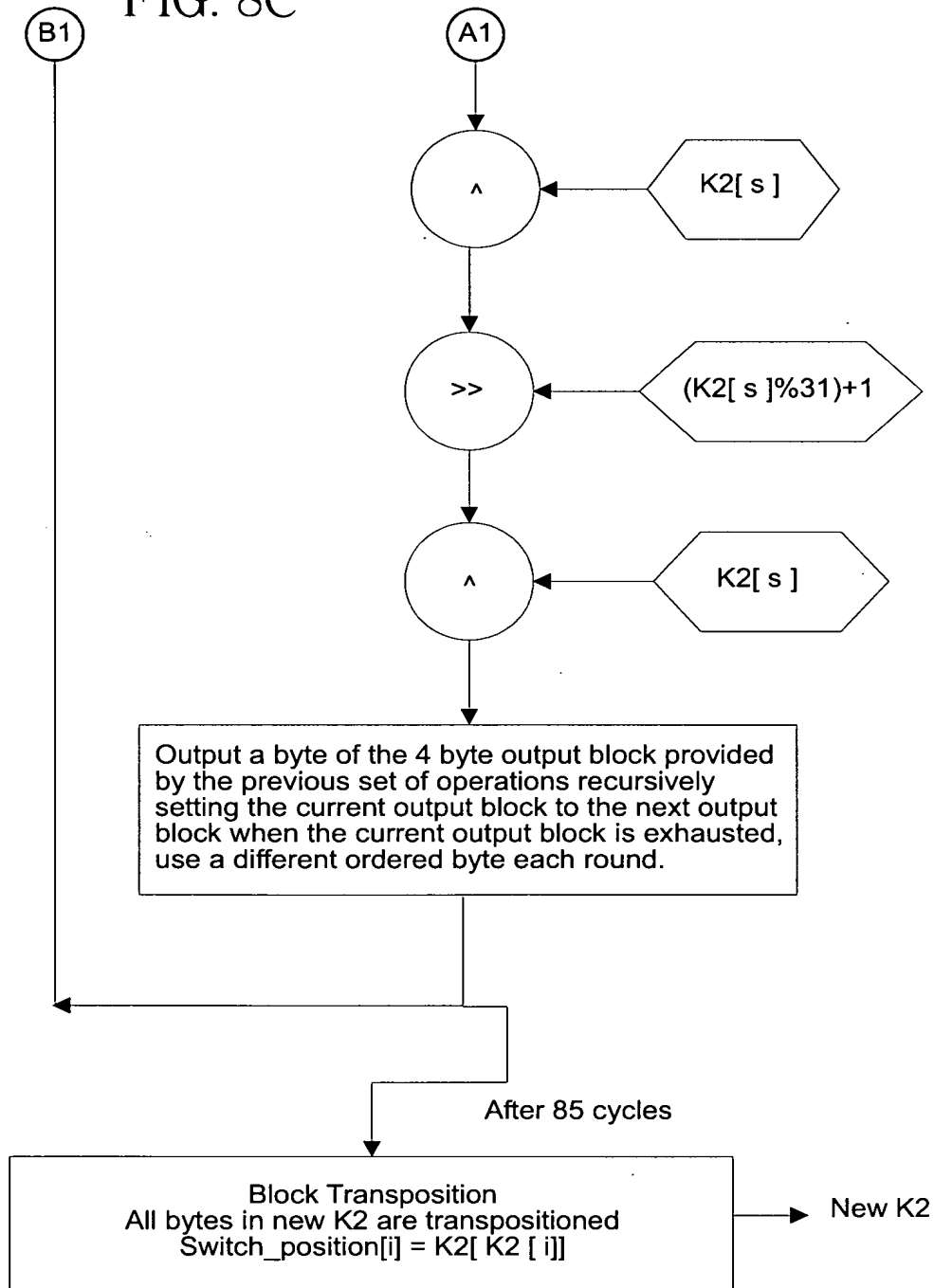


FIG. 9

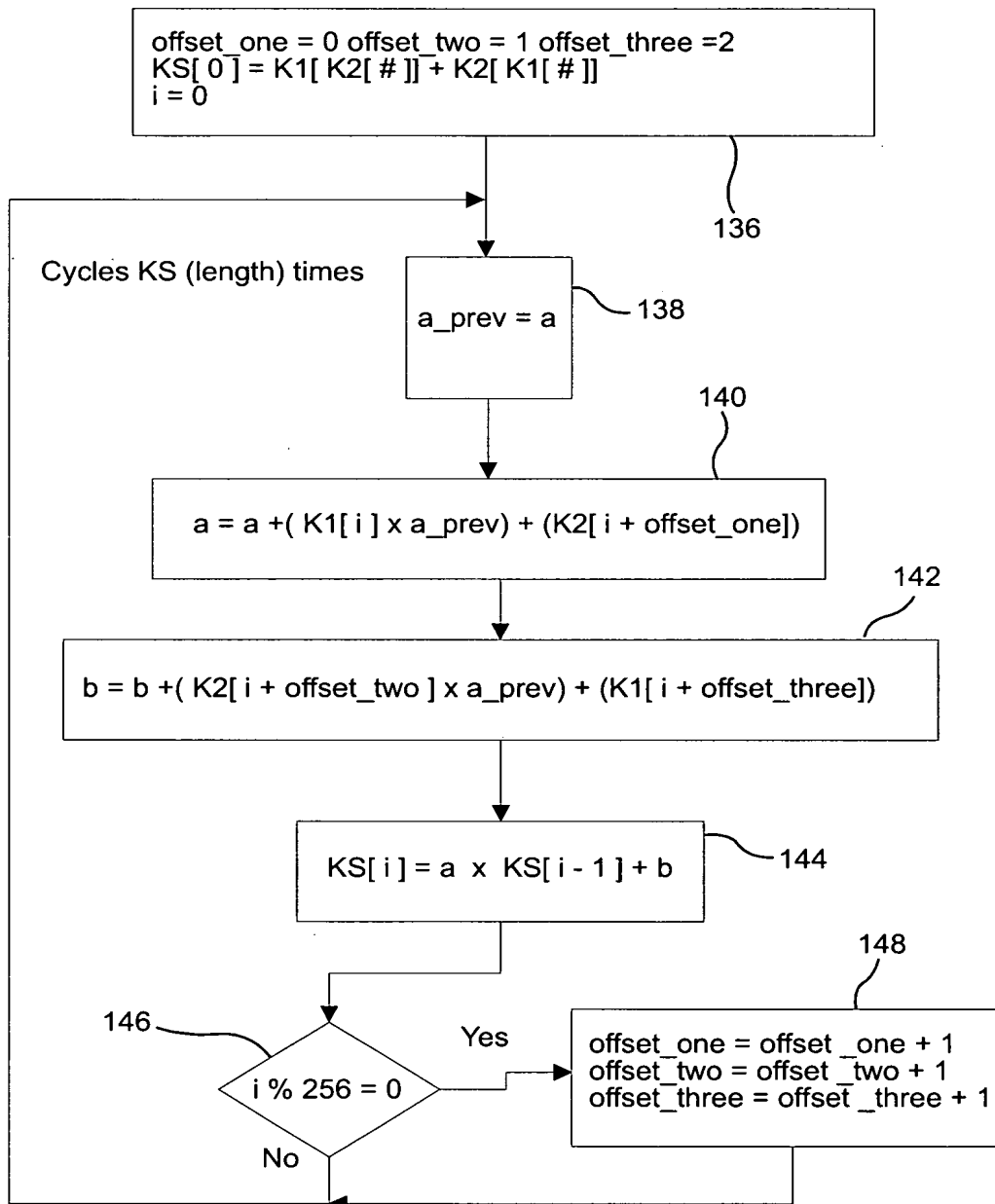


FIG. 10

150

$$\begin{aligned}
 H1(v1,v2,v3,v4,v5,v6,v7) &= (v1 \wedge v2 \& v3 \mid \sim v4 \& v5 \wedge v6 \wedge v7) \\
 H2(v1,v2,v3,v4,v5,v6,v7) &= (v1 \& \sim v2 \wedge v3 \wedge v4 \wedge v5 \& v6 \mid v7) \\
 H3(v1,v2,v3,v4,v5,v6,v7) &= (v1 \wedge v2 \mid v3 \wedge v4 \mid \sim v5 \wedge v6 \wedge \sim v7) \\
 H4(v1,v2,v3,v4,v5,v6,v7) &= (\sim v1 \wedge v2 \& v3 \mid v4 \wedge v5 \wedge \sim v6 \& v7) \\
 H5(v1,v2,v3,v4,v5,v6,v7) &= (v1 \& v2 \wedge v3 \wedge \sim v4 \mid v5 \& v6 \wedge v7) \\
 H6(v1,v2,v3,v4,v5,v6,v7) &= (v1 \wedge v2 \& \sim v3 \mid v4 \& v5 \mid v6 \wedge v7) \\
 H7(v1,v2,v3,v4,v5,v6,v7) &= (v1 \wedge v2 \mid v3 \& v4 \wedge v5 \wedge \sim v6 \& v7) \\
 H8(v1,v2,v3,v4,v5,v6,v7) &= (\sim v1 \& v2 \wedge v3 \mid v4 \wedge v5 \& v6 \wedge v7)
 \end{aligned}$$

HASH(hnum,output,v1,v2,v3,v4,v5,v6,v7,key) = (output +=  
key+hnum(v1,v2,v3,v4,v5,v6,v7))

HASH\_FOR\_KEY(hnum,result,output,v1,v2,v3,v4,v5,v6,v7,key) =  
(result+=output+key+hnum(v1,v2,v3,v4,v5,v6,v7))



FIG. 11A

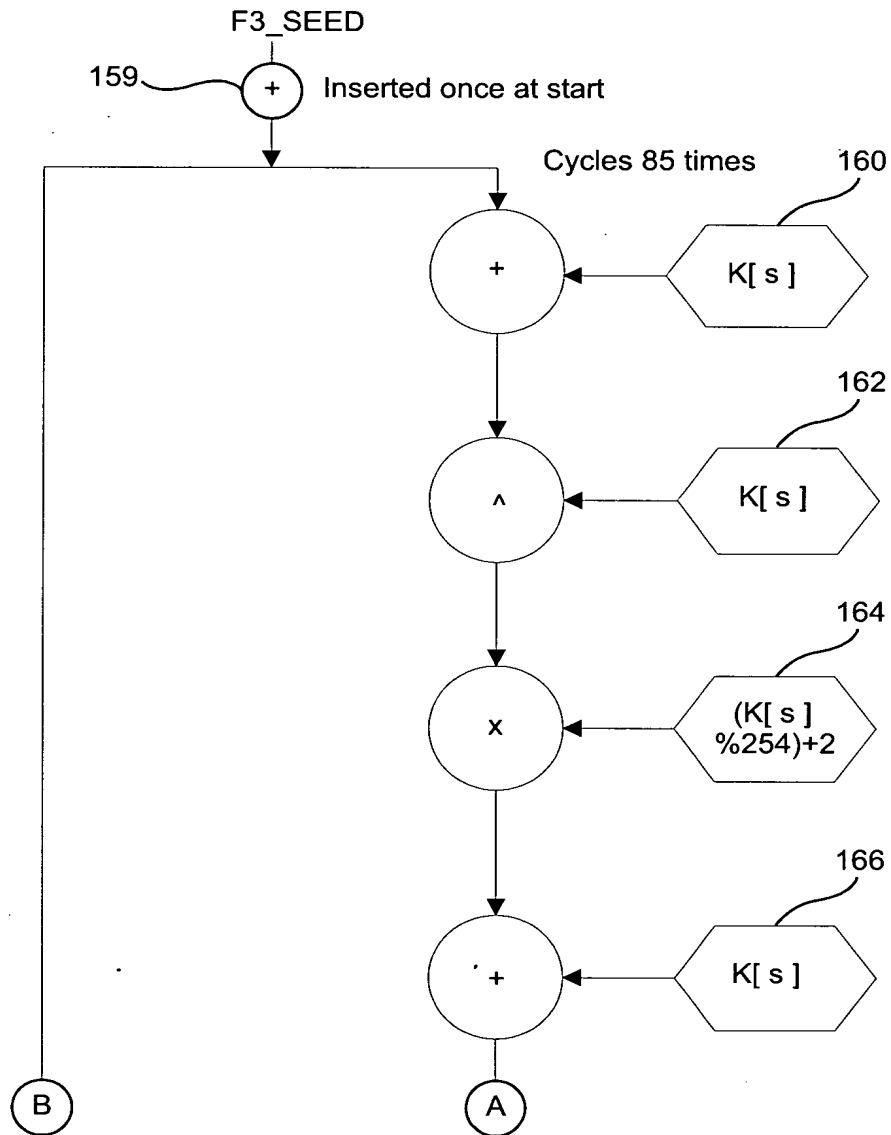


FIG. 11B

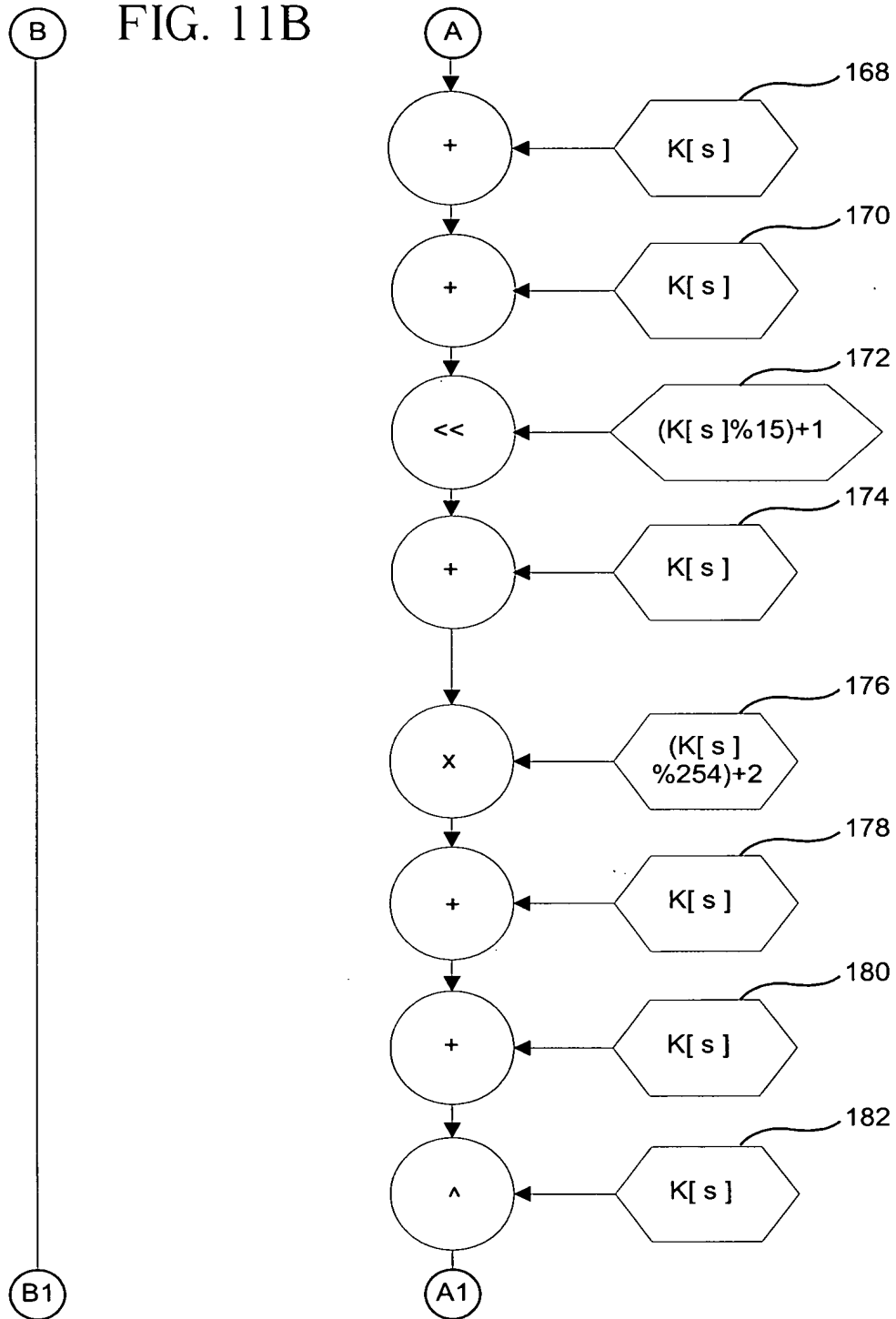
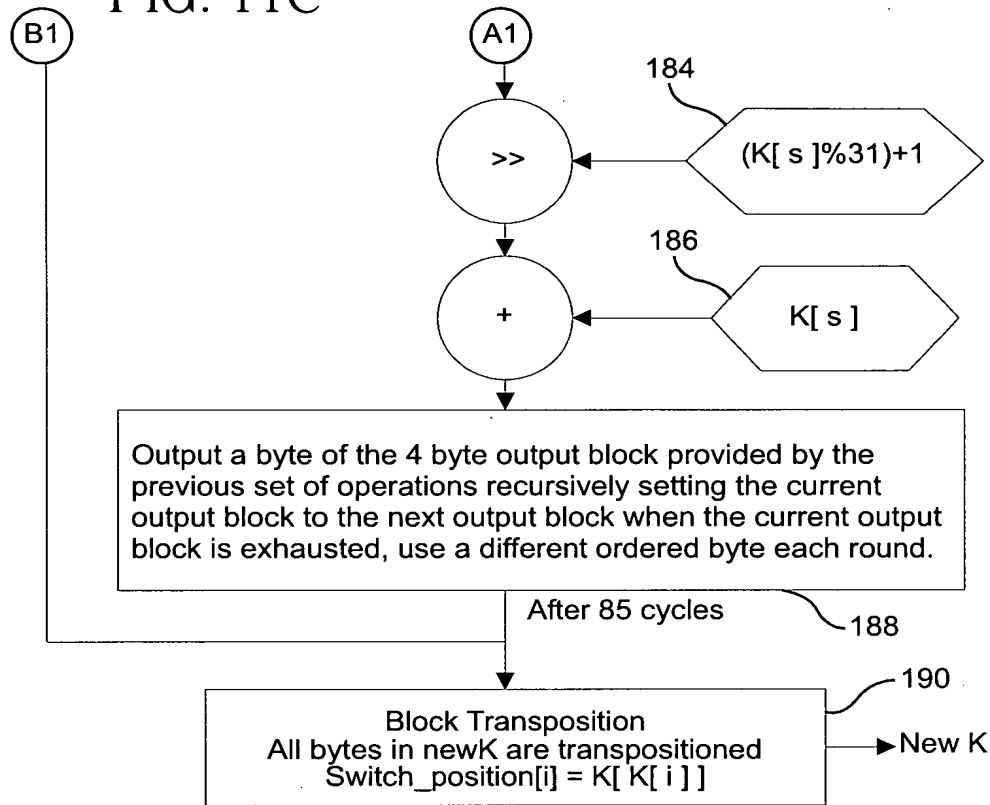


FIG. 11C



input\_block = 256 bytes of input, read from the input file.

```

var0 = 32 bit pointer assigned to input_block;
var1 = 32 bit pointer assigned to (input_block+32);
var2 = 32 bit pointer assigned to (input_block+64);
var3 = 32 bit pointer assigned to (input_block+96);
var4 = 32 bit pointer assigned to (input_block+128);
var5 = 32 bit pointer assigned to (input_block+160);
var6 = 32 bit pointer assigned to (input_block+192);
var7 = 32 bit pointer assigned to (input_block+224);

```

# - static numbers  
 index++ - running index  
 rep - running index

for(rep=0;rep<8;rep++){ - Code within "{}" will be executed eight times  
 and rep will be incremented after each loop.

FIG. 12A

```

F3_SEED = (((K[(HASH_FOR_KEY(H1,o,K[#],K[#],K[#],K[#],K[#],K[#],K[#],K[#],
K[#],K[(s)]))%64)]>>(HASH_FOR_KEY(H2,o,K[#],K[#],K[#],K[#],K[#],
K[o%64],K[#],K[#],K[(s)]))%25));

```

```

F3(F3_SEED)

```

```

F3_SEED = (((K[(HASH_FOR_KEY(H1,o,K[#],K[#],K[#],K[o%64],K[#],K[#],
K[#],K[#],K[(s)]))%64)]>>(HASH_FOR_KEY(H2,o,K[#],K[o%64],K[#],K[#],
K[#],K[#],K[#],K[#],K[(s)]))%25));

```

```

F3(F3_SEED)

```

```

F3_SEED = (((K[(HASH_FOR_KEY(H1,o,K[o%64],K[#],K[#],K[#],K[#],K[#],
K[#],K[#],K[(s)]))%64)]>>(HASH_FOR_KEY(H2,o,K[#],K[#],K[#],K[#],
K[o%64],K[#],K[#],K[(s)]))%25));

```

```

F3(F3_SEED)

```

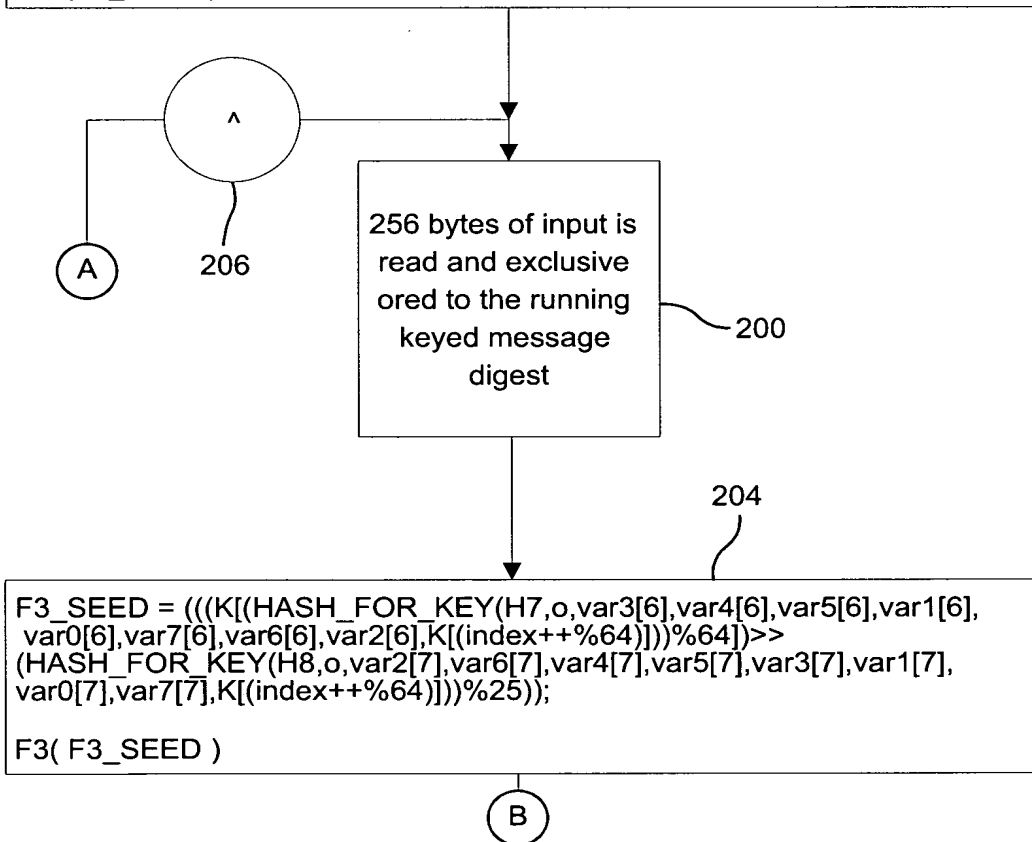


FIG. 12B

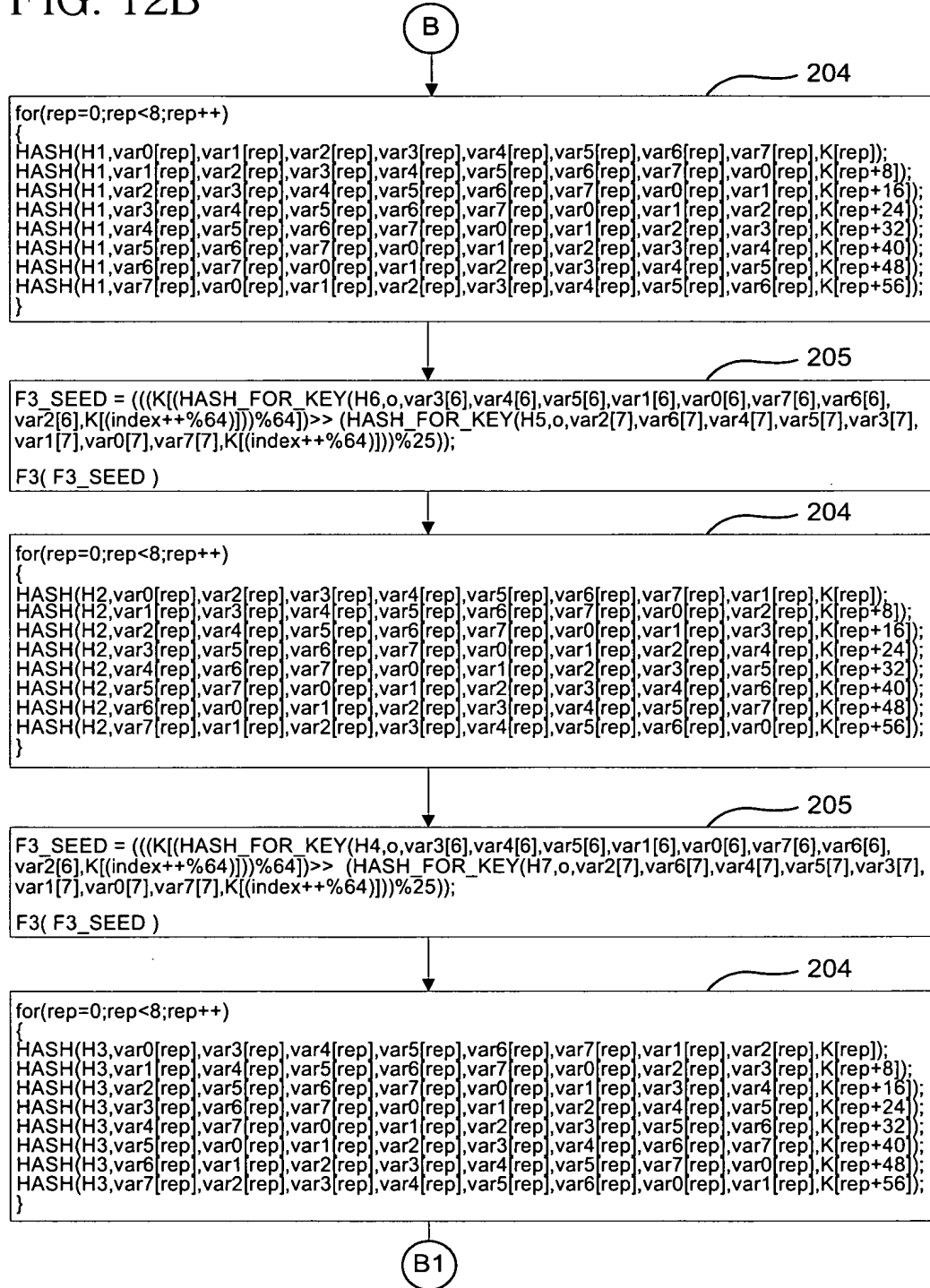


FIG. 12C

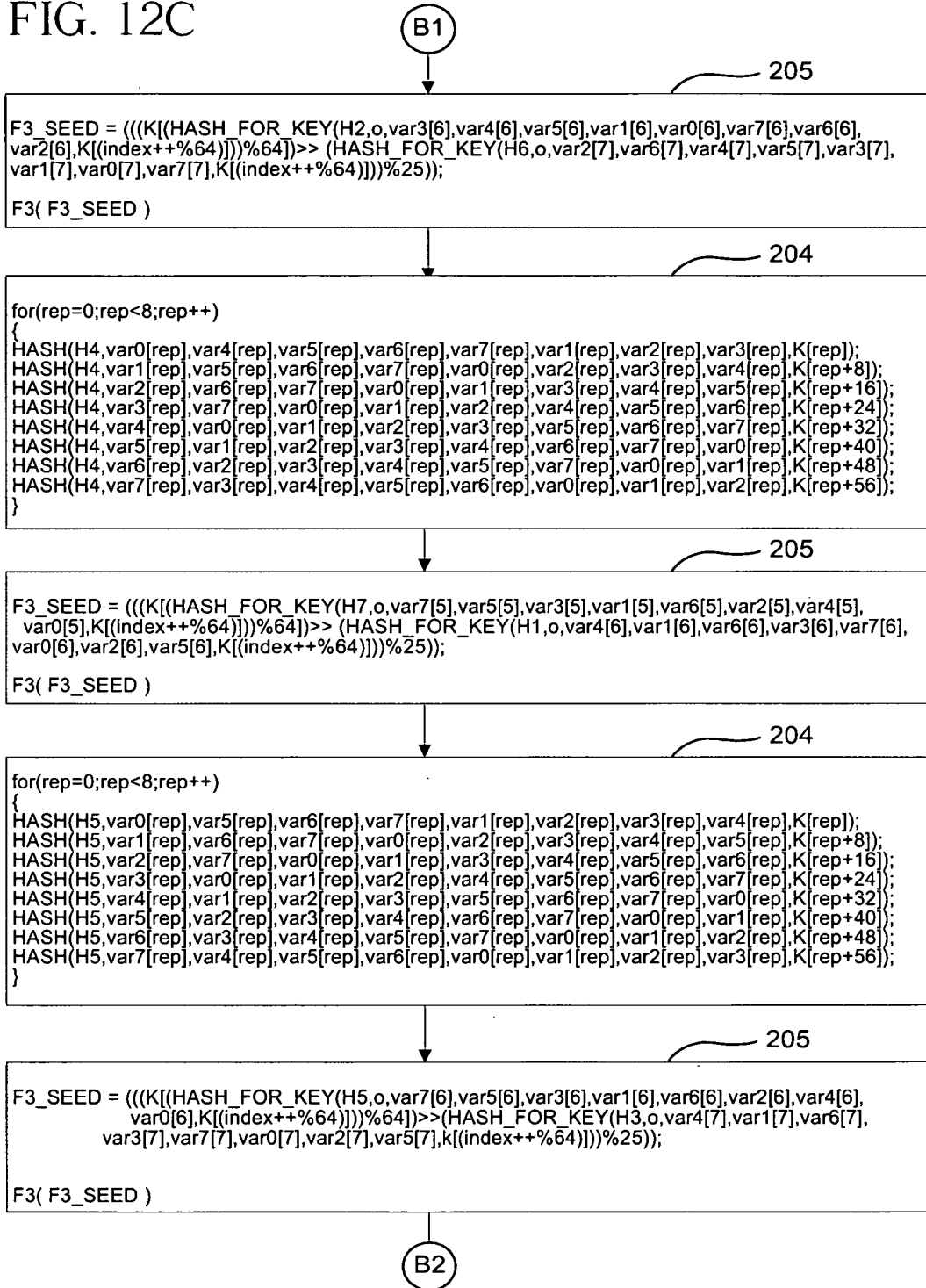


FIG. 12D

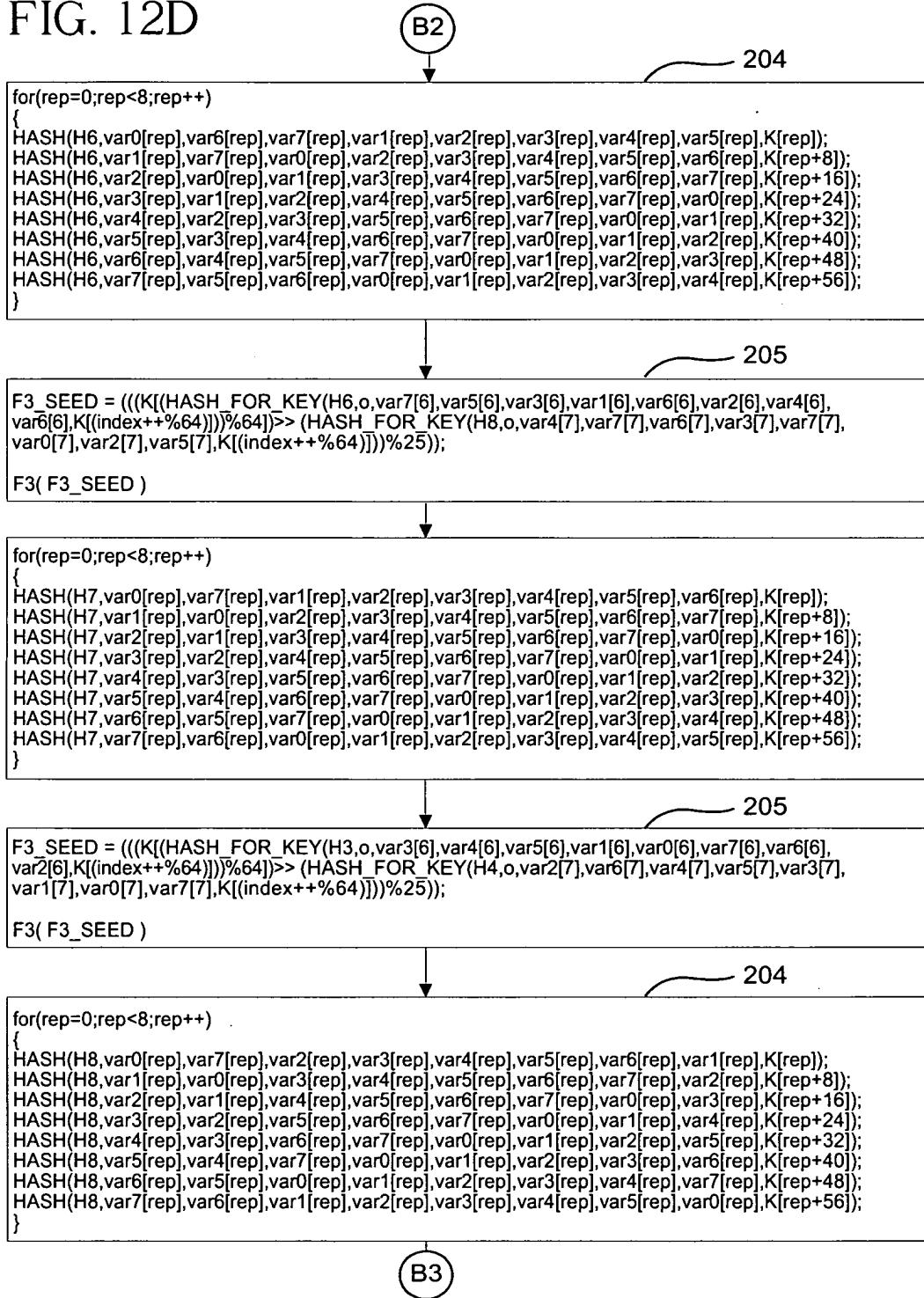


FIG. 12E

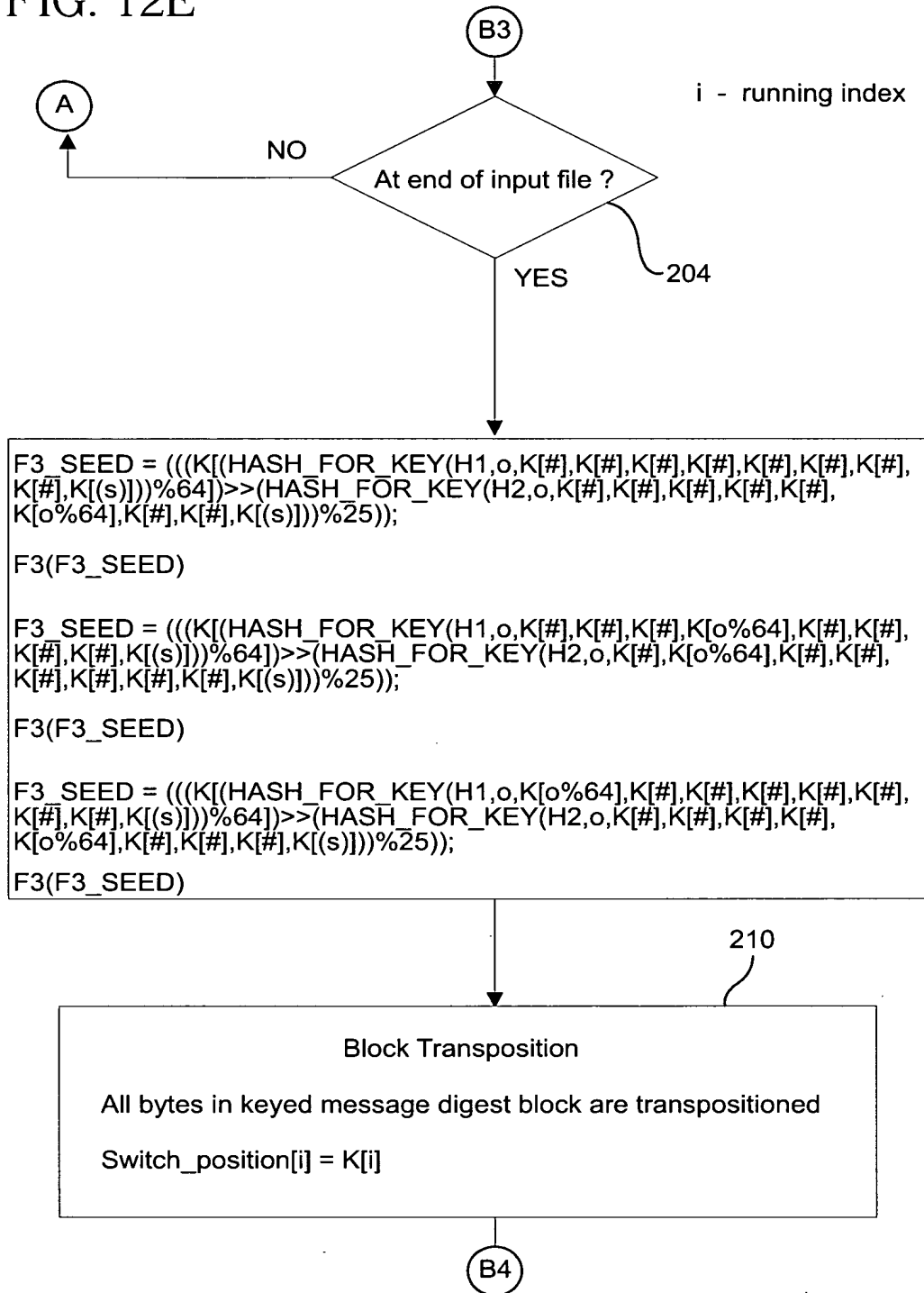




FIG. 12F

